

# Statement of Purpose

*“People often become what they believe themselves to be. If I believe I cannot do something, it makes me incapable of doing it. But when I believe I can, then I acquire the ability to do it even if I did not have it in the beginning.”*

-Mohandas Gandhi

My aspiration to pursue a graduate program in your University is underscored by the fact that it offers the flexibility needed for an in-depth understanding of the vast and rapidly changing field of Engineering Technology.

My under-graduate syllabi at [REDACTED] college of engineering and technology, one of the most prestigious institutions in the country, exposed me to all the core areas of Electronics and Communications Engineering like Signals and systems, Network analysis, Electronics circuit analysis, Pulse and Digital circuits, control systems, Object oriented programming, switching theory and logic design, Analog communications, Computer organization, Computer networks, Radar systems, Wireless communication and networks. These courses have given me a good foundation in the core concepts. I have also been made acquainted with programming languages like C & C++ and operating systems as Windows & UNIX. The first two years of undergraduate study was based on fundamentals of all engineering subjects and next two years was a detailed study of electronics and communication subjects. Though all the subjects were different in their theories, all the subjects were interlinked in few aspects. In these four year undergraduate studies, I gained in depth understanding of the various disciplines in engineering.

The final year project was very interesting. It is my Study Project on “Automated test set up for VME based Analog I/O cards” in my final year. It gave me immense pleasure to do a project with the integration of both Electronics and Computers.

In this project, I dealt with the testing of analog input and output cards using field programmable gate array(FPGA). The FPGA is the heart of the project, it mainly has three sections such as input section, control section, output section. In the input section, the analog signal is converted to the digital signal by using an ADC. In the control section the interfacing is done to the FPGA and the Analog input output signals. In the output section the testing is done and the analog signal is converted into a digital signal.

I have learnt many things by doing a project on my own. It gave me exposure to the subjects what I learnt in the undergraduation.

I am applying what I have learnt so far but as the world changes each hour, I want to stay up to the mark and this is what I expect I can do from my higher education. It'll give me insight to focus on technical solutions in wider perspectives and implementations of what this world needs and where we can take it through our talent.

I enjoy doing investigative and innovative work, especially when it leads to practical applications. My ideal vision of a career has always been a job that involves creativity, innovation and flexibility. I am confident that higher education would give me the desired push towards a career in research and an opportunity to do well in my chosen field of interest. I am eager to work under the guidance of the distinguished faculty members of your University. I believe that a graduate program, your University, will help me reach my goals.

I am self motivated, patient, diligent and have penchant to explore new technologies. I feel getting an opportunity in pursuing masters in your esteemed university enhances my skills and let me to apply my skills to contribute to research. The university facilitates to shape my career to crack down my goals and reach the pinnacle.